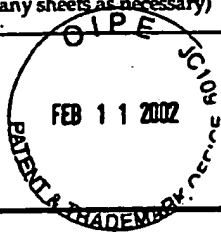


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		Filing Date: December 5, 2001	



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		Number	Kind Code ² (If known)			
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		Office ³	Number ⁴	Kind Code ⁵ (If known)				
		EP	0415542 A1	A1	Advanced Micro Devices, Inc.	03/06/91		
		EP	0630115 A2	A2	Pilkington Micro-Electronics Limited	12/21/94		
		GB	2180382 A		Pilkington Micro-Electronics Limited	03/25/87		
		GB	2295738 A		Altera Corporation	06/05/96		

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			WO94/28475	A1	Regents of the University of California	12/08/94		
			WO96/05964	A1	Minnesota Mining and Manufacturing Company	02/29/96		
			WO96/35261	A1	BTR, Inc.	11/07/96		

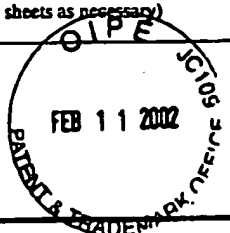
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OTHER ART - NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
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		DEVADAS, S., "Boolean Decomposition of Programmable Logic Arrays", IEEE 1988, pp. 2.5.1-2.5.5.	
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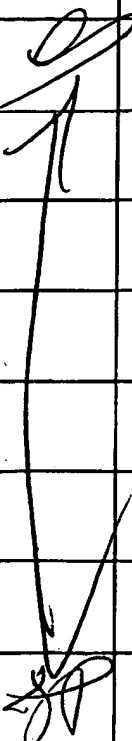
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		YACHYANG SUN et al., "An Area Minimizer for Floorplans with L-Shaped Regions", 1992, Int'l. Conference on Computer Design, pp. 383-386.	
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